

A data carrier (DC) has a receiving-means configuration (RC) which includes a switching means (S) and a first transmission coil (L1), which can be short-circuited with the aid of the switching means (S), and at least one second transmission coil (L2), which is arranged in series with the first transmission coil (L1), and capacitor configuration (CC), which is arranged in parallel with at least the second transmission coil (L2), the receiving means configuration (RC) being configured to be controllable as regards the value of at least one of its elements comprising the at least one second transmission coil (L2) and the capacitor configuration (CC).

Fig. 2